

L Number	Hits	Search Text	DB	Time stamp
3	5392	((controller (control near3 (logic circuit))) same (access\$3 read\$3 load\$3 writ\$3 stor\$3) same (different with (entry line location)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/08/24 21:58
4	740	((concurrent43 simultaneous\$3 ("SAME" adj3 time) parallel) same ((controller (control near3 (logic circuit))) same (access\$3 read\$3 load\$3 writ\$3 stor\$3) same (different with (entry line location)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/08/24 22:10
5	94	711/.cccls. and ((concurrent43 simultaneous\$3 ("SAME" adj3 time) parallel) same ((controller (control near3 (logic circuit))) same (access\$3 read\$3 load\$3 writ\$3 stor\$3) same (different with (entry line location))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/08/24 22:12
6	36	way and ((DMA (direct\$3 adj2 memory adj2 access)) near4 controller) and ((concurrent43 simultaneous\$3 ("SAME" adj3 time) parallel) same ((controller (control near3 (logic circuit))) same (access\$3 read\$3 load\$3 writ\$3 stor\$3) same (different with (entry line location))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/08/24 22:12